

**North South University**

**Department of Electrical & Computer Engineering**

**Lab Report**

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| **Experiment No:** | 09 |
| **Experiment Title:** | Synchronous Sequential Circuits |

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| **Course Code:** | **CSE231L** |
| **Section:** | **08** |
| **Course Name:** | **Digital Logic Design Lab** |

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| **Lab Group #:** | 01 |
| **Written By:** | Aziza Zaman Sadia (ID: 2231603642) |

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| **Date of Experiment:** | 27.07.2025 |
| **Date of Submission:** | 10.08.2025 |
| **Submitted To:** | Tushar Basak |

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| **Group Members ID** | **Group Members Name** |
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